	Туре	Hits	Search Text	DBs
1	BRS	275	(dynamic or domino) and logic and precharge and keeper	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB
2	BRS	155	S1 and nor	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM TDB
3	BRS	3	("5481495"   "6038193"   "6215694").PN.	US-PGPUB; USPAT; USOCR
4	BRS	1	("6320795").URPN.	USPAT
5	BRS	11	("4426699"   "4617664"   "4692893"   "4694274"   "4857882"   "5130578"   "5319348"   "5341314"   "5590361"   "5619642"   "5719889").PN.	US-PGPUB; USPAT; USOCR
6	BRS	3	("5881076").URPN.	USPAT
7	BRS	3	("5872467"   "5881076"   "5910762").PN.	US-PGPUB; USPAT; USOCR
8	BRS	0	("6456116").URPN.	USPAT
9	BRS	3	("5281946"   "5319348"   "5534844").PN.	US-PGPUB; USPAT; USOCR
10	BRS	2	("5910762").URPN.	USPAT
11	BRS	6	("5617348"   "5694362"   "5729190"   "5754463"   "5761140"   "5764089").PN.	US-PGPUB; USPAT; USOCR
12	BRS	3	("5477489"   "6038193"   "6320795").PN.	US-PGPUB; USPAT; USOCR
13	BRS	0	("6388940").URPN.	USPAT

	Туре	Hits	Search Text	DBs
			(((global adj line) and	US-PGPUB; USPAT;
1	BRS	13035	(local adj line)) or ((bit	USOCR; FPRS;
	DKS	13033	adj line) and (data adj	EPO; JPO;
			line)))	DERWENT; IBM TDB
2		4174	S1 and (precharge or pre- charge)	US-PGPUB; USPAT;
	BRS			USOCR; FPRS;
	DKS			EPO; JPO;
				DERWENT; IBM_TDB
		872	S5 and 365/185.23,189.09,189.11,20 3,230.06.ccls.	US-PGPUB; USPAT;
3	BRS			USOCR; FPRS;
	DKS			EPO; JPO;
				DERWENT; IBM TDB
4				US-PGPUB; USPAT;
	BRS	4155	S4 and (precharge or pre-	USOCR; FPRS;
3	DV2	4100	charge)	EPO; JPO;
				DERWENT; IBM_TDB
			1	US-PGPUB; USPAT;
5	BRS	12956		USOCR; FPRS;
5				EPO; JPO;
				DERWENT; IBM_TDB
		873	S2 and 365/185.23,189.09,189.11,20 3,230.06.ccls.	US-PGPUB; USPAT;
6	BRS			USOCR; FPRS;
				EPO; JPO;
				DERWENT; IBM_TDB
		810	1S6 and transistor	US-PGPUB; USPAT;
7	BRS			USOCR; FPRS;
				EPO; JPO;
				DERWENT; IBM_TDB
	BRS	510	S7 and latch	US-PGPUB; USPAT;
8				USOCR; FPRS;
	BIG			EPO; JPO;
				DERWENT; IBM_TDB
	BRS		((bit adj line) and (data adj line))	US-PGPUB; USPAT;
9		12991		USOCR; FPRS;
				EPO; JPO;
				DERWENT; IBM_TDB
		4173	S14 and (precharge or pre- charge)	US-PGPUB; USPAT;
10	BRS			USOCR; FPRS;
				EPO; JPO;
	<u> </u>			DERWENT; IBM_TDB

	Туре	Hits	Search Text	DBs
11	BRS	875	S15 and 365/185.23,189.09,189.11,20 3,230.06.ccls.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM TDB
12	BRS	813	S16 and transistor	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM TDB
13	BRS	513	S17 and latch	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM TDB
14	BRS	13070	(((global adj line) and (local adj line)) or ((bit adj line) and (data adj line)))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB

## **EAST Search History**

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	0	((bitline or (bit adj line)) and (data adj bus) and (charg\$3 or precharg\$3 or (Pre adj charg\$3)) and (data adj signal) and (pulldown or (pull adj down))).clm.	US-PGPUB	OR	ON	2006/08/02 09:27
L2	4	((bitline or (bit adj line)) and (data adj bus) and (charg\$3 or precharg\$3 or (Pre adj charg\$3)) and (pulldown or (pull adj down))).clm.	US-PGPUB	OR	ON	2006/08/02 09:27
L3	0	((bitline or (bit adj line)) and (data adj bus) and (charg\$3 or precharg\$3 or (Pre adj charg\$3)) and (pulldown or (pull adj down)) and transistor and (logic adj circuit\$2)).clm.	US-PGPUB	OR	ON	2006/08/02 09:27
L4	0	((bitline or (bit adj line)) and (data adj bus) and (charg\$3 or precharg\$3 or (Pre adj charg\$3)) and (pulldown or (pull adj down)) and transistor and (NOR)).clm.	US-PGPUB	OR	ON	2006/08/02 09:28
L5	0	((bitline or (bit adj line)) and (data adj bus) and (charg\$3 or precharg\$3 or (Pre adj charg\$3)) and (pulldown or (pull adj down)) and (equilibration)). clm.	US-PGPUB	OR	ON	2006/08/02 09:28
L6	4	((bitline or (bit adj line)) and (data adj bus) and (charg\$3 or precharg\$3 or (Pre adj charg\$3)) and (pulldown or (pull adj down)) and (data)).clm.	US-PGPUB	OR	ON	2006/08/02 09:29
L7	0	((bitline or (bit adj line)) and (data adj bus) and (charg\$3 or precharg\$3 or (Pre adj charg\$3)) and (pulldown or (pull adj down)) and (data adj signal)).clm.	US-PGPUB	OR	ON	2006/08/02 09:29
L8	3	((bitline or (bit adj line)) and (data adj bus) and (charg\$3 or precharg\$3 or (Pre adj charg\$3)) and (pulldown or (pull adj down)) and gate).clm.	US-PGPUB	OR	ON	2006/08/02 09:29
L9	2	((bitline or (bit adj line)) and (data adj bus) and (charg\$3 or precharg\$3 or (Pre adj charg\$3)) and (pulldown or (pull adj down)) and transistor and gate).clm.	US-PGPUB	OR	ON	2006/08/02 09:29
L10	4	2 or 6 or 8 or 9	US-PGPUB	OR	ON	2006/08/02 09:30